

Fig. 1

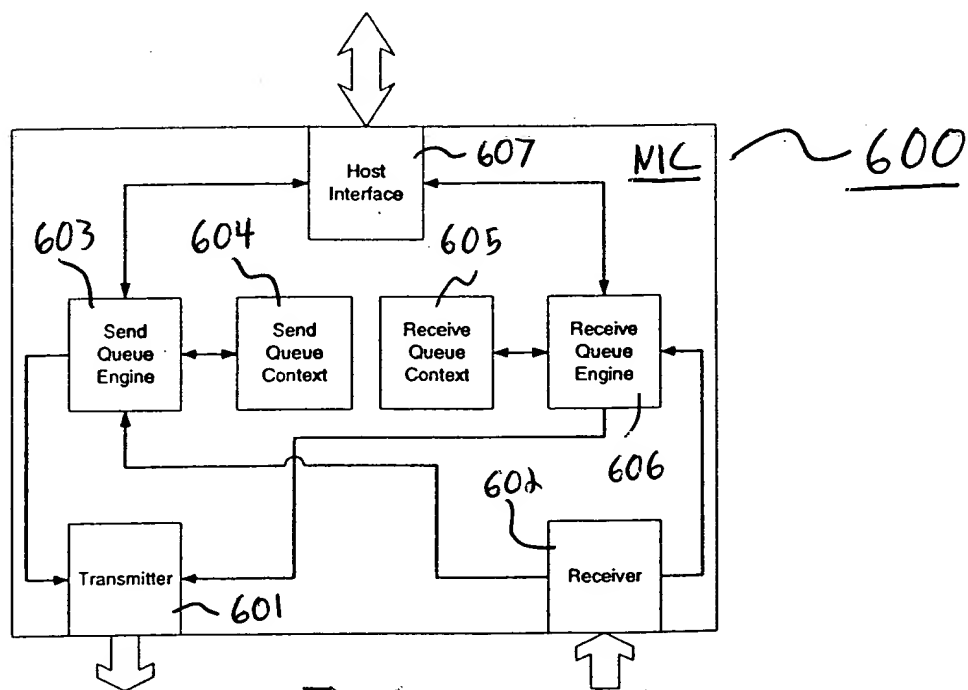


Fig. 6

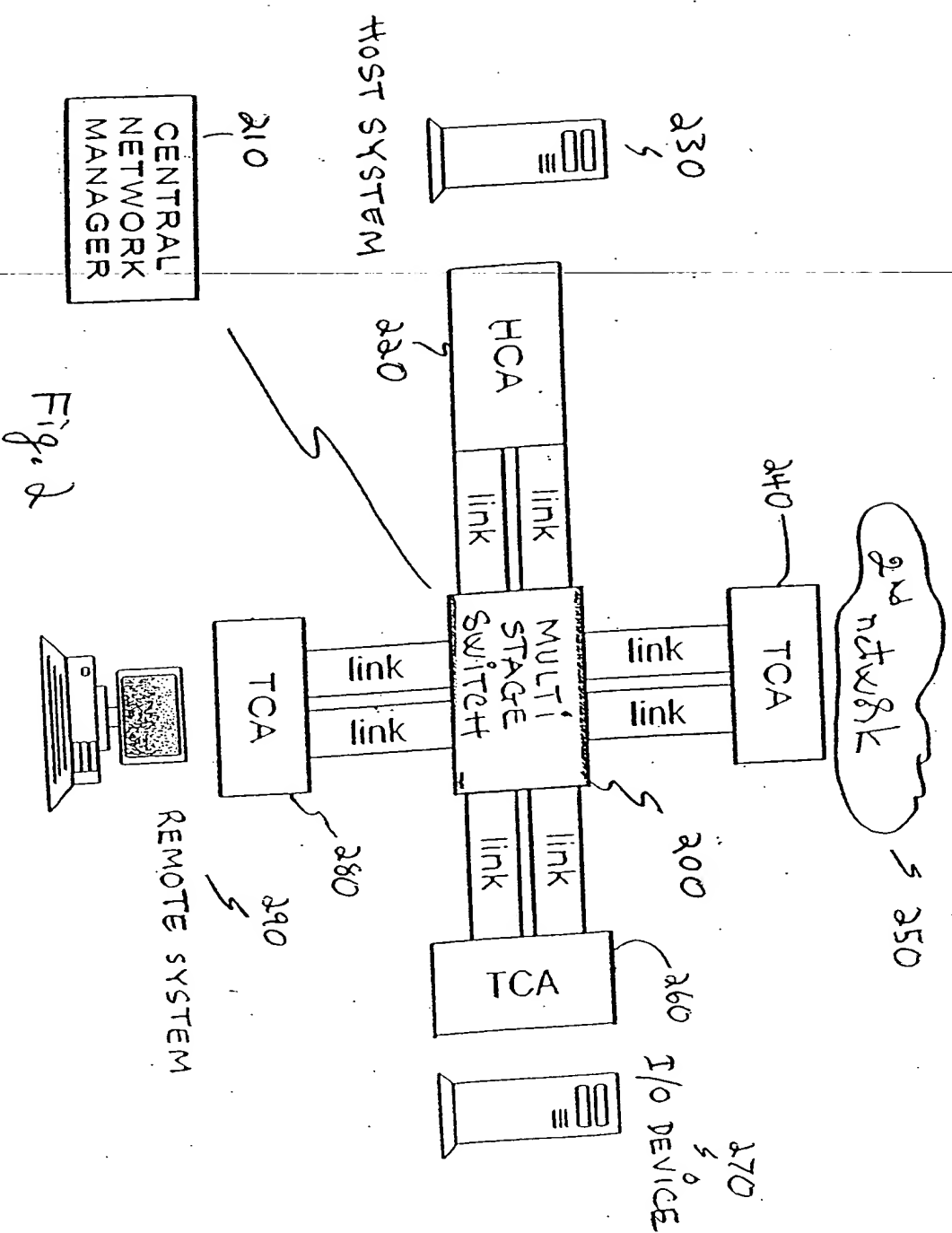


Fig. 2

FIG. 2 is a block diagram of a network architecture in accordance with the present invention. The network architecture includes a host system 230, a remote system 290, a central network manager 210, a multi-stage switch 200, and a second network 250. The host system 230 is connected to the multi-stage switch 200 via a host control agent 220. The remote system 290 is connected to the multi-stage switch 200 via a terminal control agent 280. The central network manager 210 is connected to the multi-stage switch 200. The multi-stage switch 200 is connected to the second network 250 via a terminal control agent 240 and to an I/O device 270 via a terminal control agent 260.

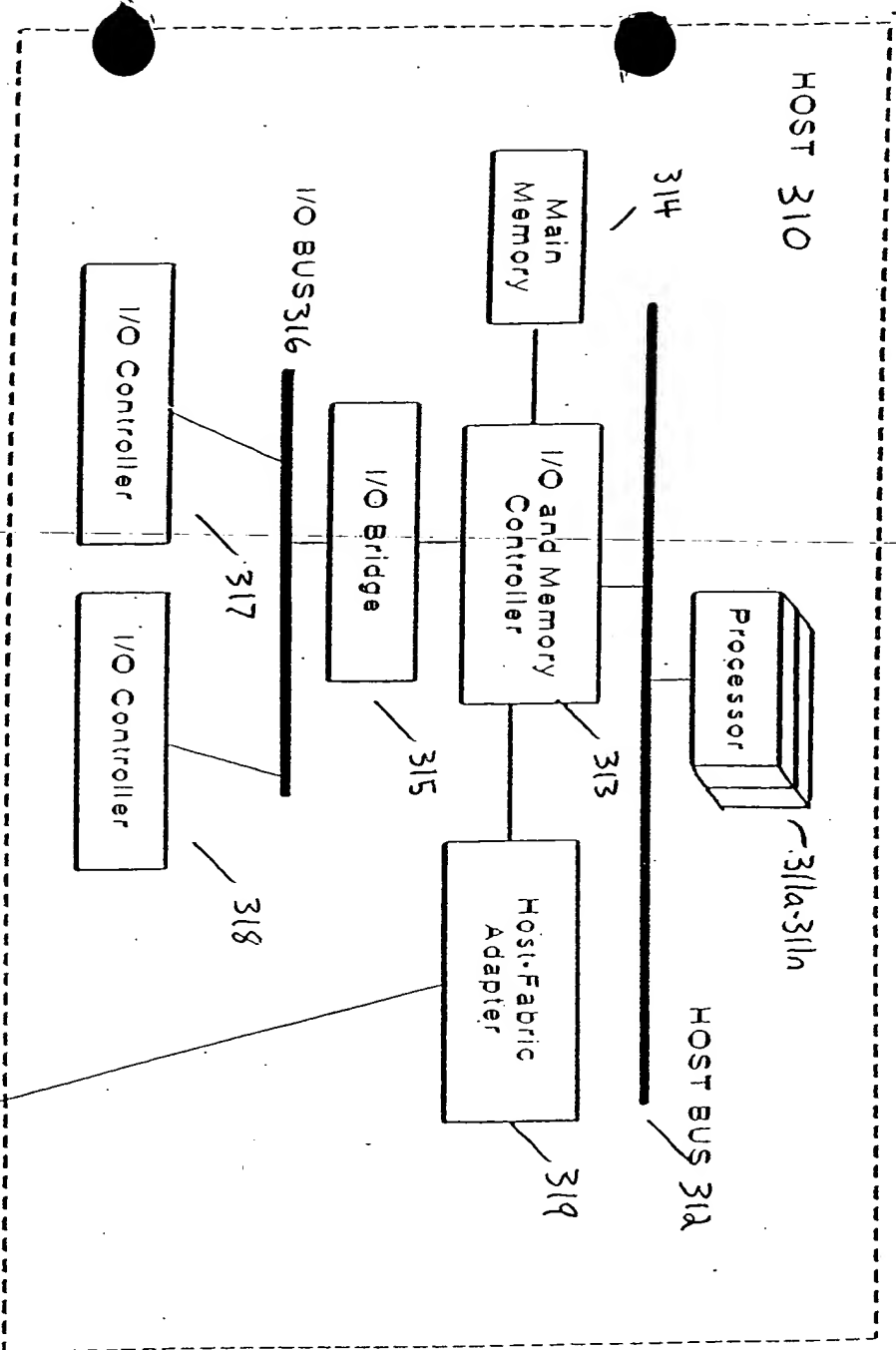
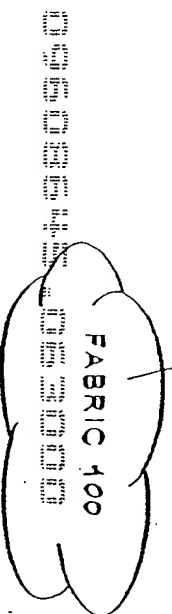


Fig. 3



HOST 310

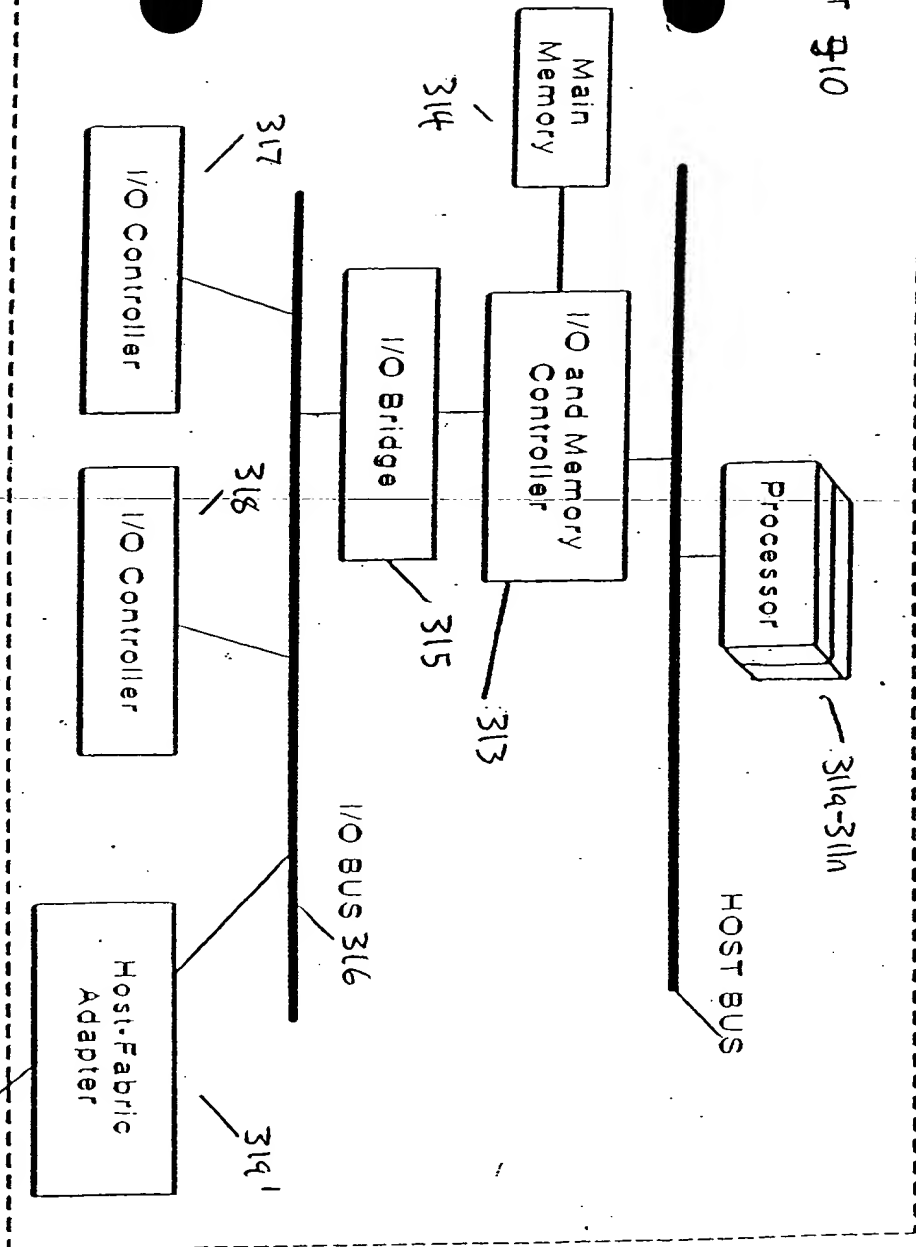


Fig. 4

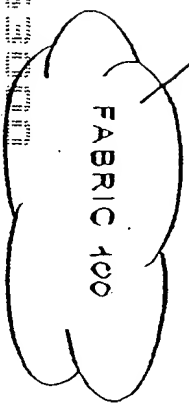


FIG. 4 is a block diagram of a host system 310, which is connected to a fabric 100. The host system 310 includes a processor 314-311n, an I/O and memory controller 313, main memory 314, an I/O bridge 315, an I/O bus 316, and two I/O controllers 317 and 318. The host system 310 is also connected to a host-fabric adapter 314' which interfaces with the fabric 100.

500

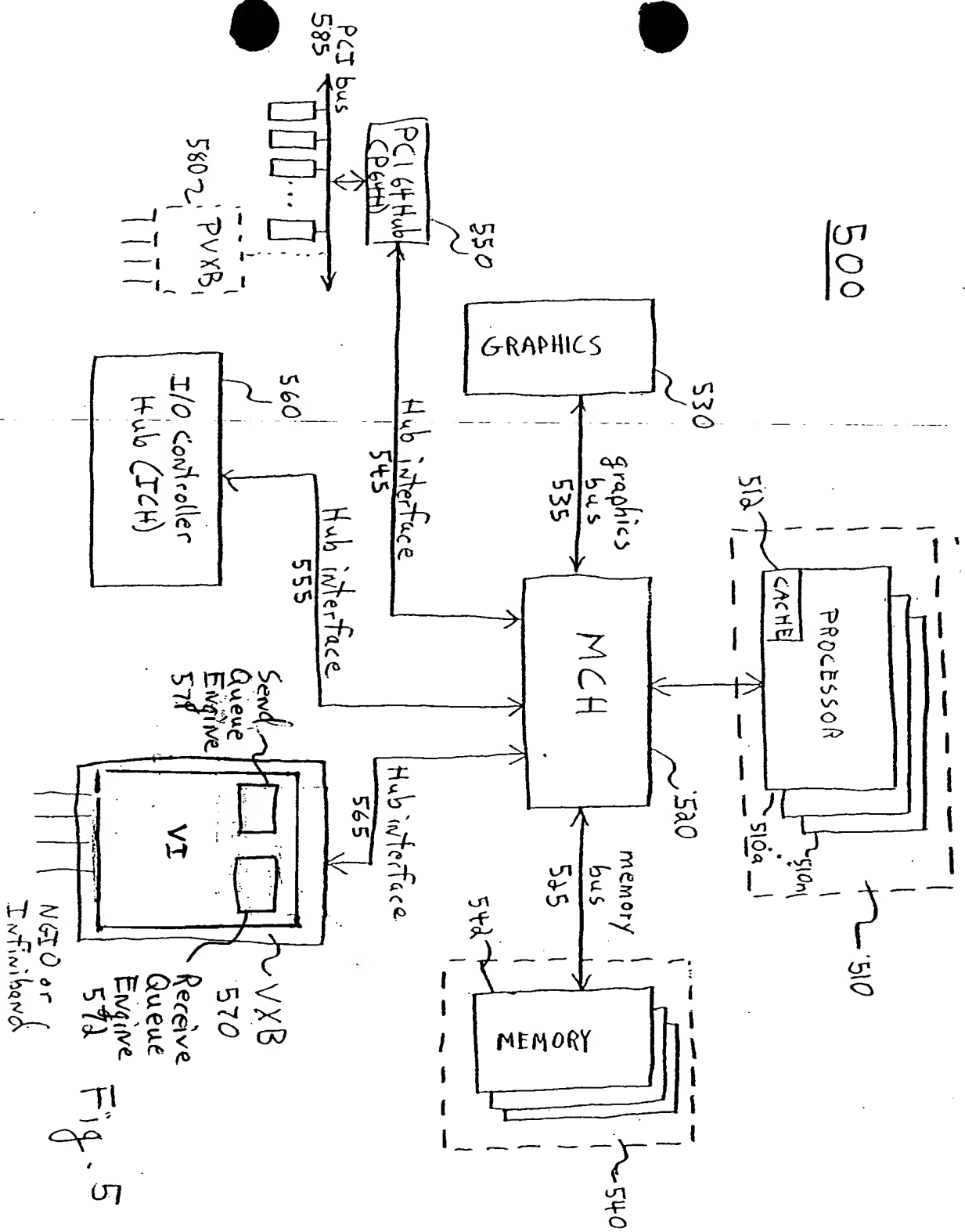


Fig. 5

FIG. 5 is a block diagram of a system architecture. The system includes a processor (510) connected to a memory controller hub (MCH) (520). The MCH is connected to a graphics card (530) via a graphics bus (535) and to memory (540) via a memory bus (545). The MCH also connects to a PCI 64-bit hub (CP64H) (550) via a hub interface (545), which in turn connects to a PCI bus (585) with various devices (580, 581, 582, 583, 584, 585). An I/O controller hub (ICH) (560) is connected to the MCH via a hub interface (555) and to the PCI bus (585). A video interface (VI) (570) is connected to the MCH via a hub interface (565) and includes a send queue engine (571) and a receive queue engine (572). The VI is also connected to a VXB (570) and an NGIO or Infiniband interface.

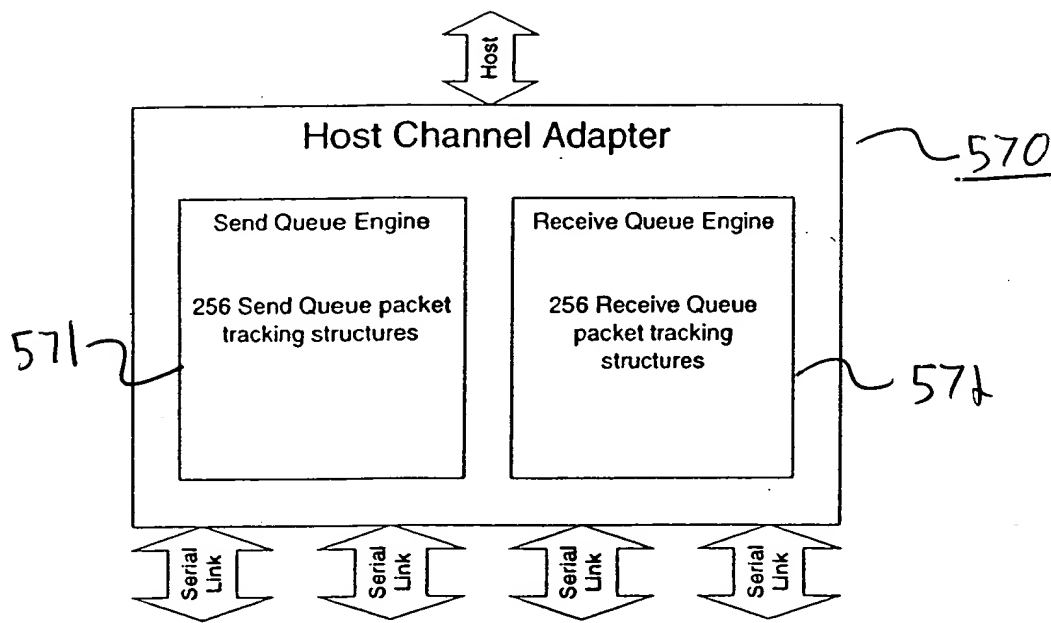


Fig. 7

